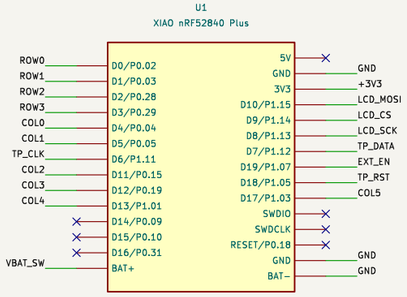
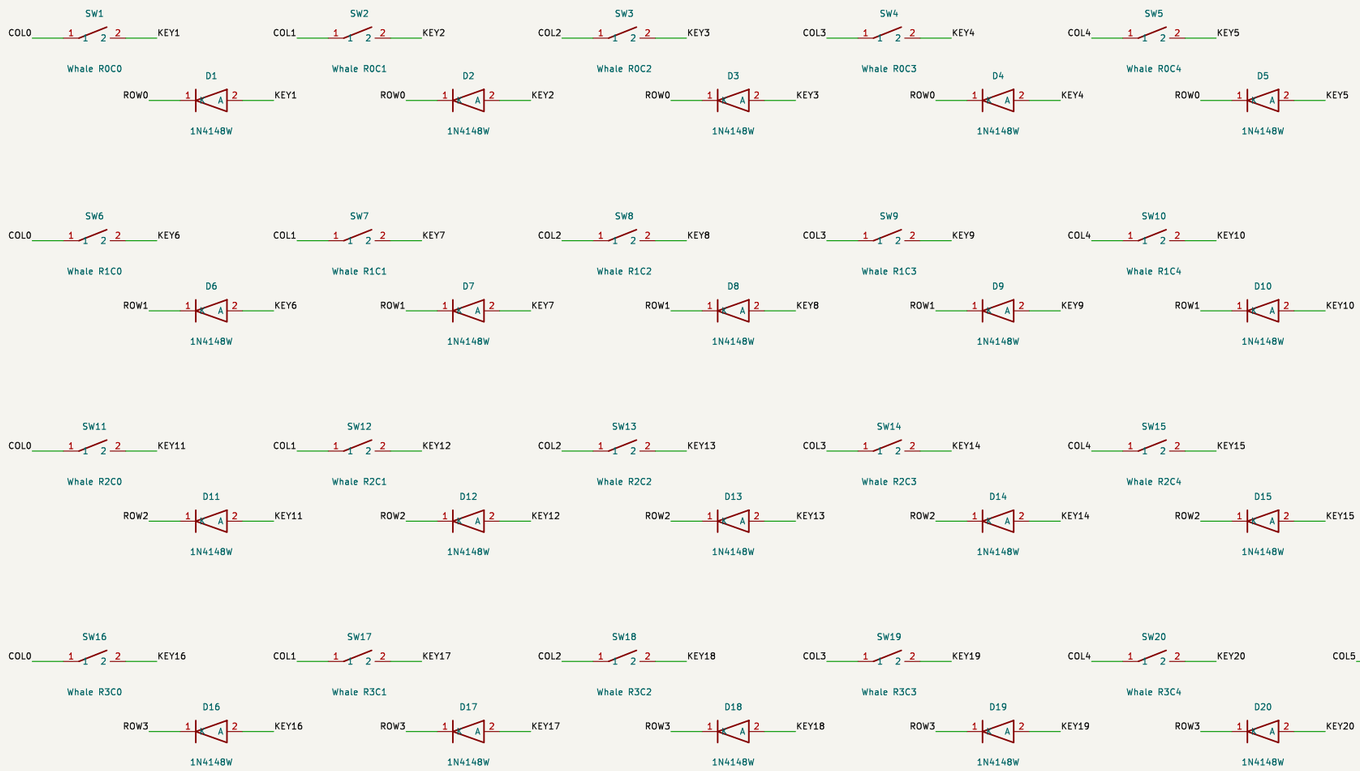
21-key matrix, COL2ROW: diode cathode to ROW

XIAO Plus, 50 mA charging; P0.13 HIGH/input, never LOW



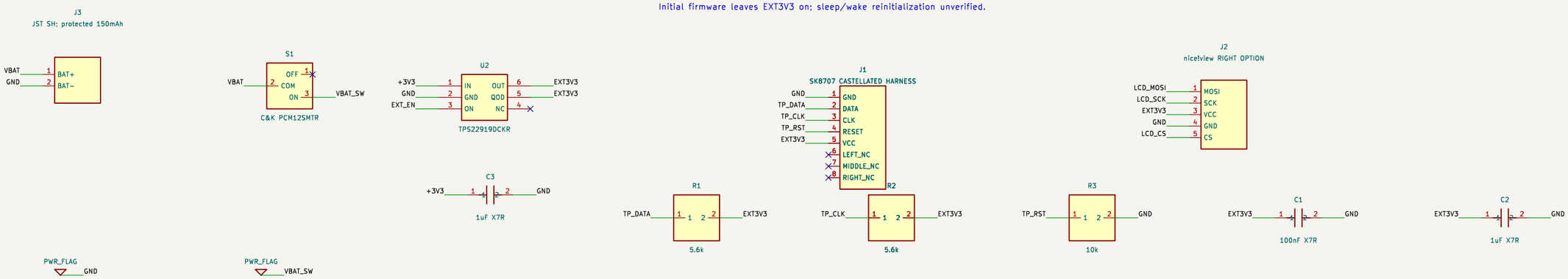
Battery ADC P0.31 reserved; P0.14 LOW in application. Check reset/bootloader.



Right central to host USB/BLE. Left split BLE with own USB charging.

J1 follows CASTELLATED driver pads, not the alternate FFC pinout.

Initial firmware leaves EXT3V3 on; sleep/wake reinitialization unverified.



JST SH polarity: pin 1 BAT+, pin 2 GND. No inter-half power link.

UNTESTED HARDWARE: verify module joints, battery, firmware and mechanical fit

Sheet: /
File: ergopoint42_p1_right.kicad_sch

Title: ErgoPoint42 right compact prototype P1

Size: A3	Date: 2026-09-07	**Rev: P1 UNBUILT**
KiCad E.D.A. 9.0.8		Id: 1/1